



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **AKAMATSU, et al.**

Serial No.: **09/392,722**

Filed: **September 9, 1999**

Group Art Unit: **2827**

Examiner: **GRAYBILL, David E.**

P.T.O. Confirmation No.: 1679

For. **INTEGRATED ELECTRONIC DEVICE HAVING FLIP-CHIP CONNECTION  
WITH CIRCUIT BOARD AND FABRICATION METHOD THEREOF**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
Washington, D.C. 20231

Date: April 8, 2003


Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated October 8, 2002 for a third month from March 8, 2003 to April 8, 2003 (\$410.00 for a two month extension of time was filed on March 10, 2003).

Attached please find a check in the amount of \$520.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

**ARMSTRONG, WESTERMAN & HATTORI, LLP**

  
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520.00 DP

SGA/arf  
Atty. Docket No. **950637B**  
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PATENT TRADEMARK OFFICE

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